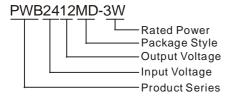
MORNSUN®

PWA_(M)D-3W&PWB_(M)D-3W Series 3W, WIDE INPUT, ISOLATED & REGULATED DUAL/SINGLE OUTPUT DC-DC CONVERTER



Patent Protection RoHS

PART NUMBER SYSTEM



FEATURES

- 4:1 wide input range
- Operating temperature Range: -40°C to +85°C
- 1.5KVDC isolation
- Short circuit protection (automatic recovery)
- UL94-V0 package
- No external component required
- Industry standard pinout
- Five sides metal shielding (PWA/B_MD)
- MTBF>1,000,000 hours

APPLICATIONS

The PWA_(M)D-3W & PWB_(M)D-3W Series are designed for application where isolated output is required from a distributed power system.

These products apply to where:

- 1) Input voltage ranges≤ 4:1;
- 2) 1.5KVDC input and output isolation;
- 3) Regulated and low ripple noise is required.

SELECTION G	IIDE									
	Input Voltag	ge(VDC)	Output	Output Cu	urrent (mA)	Input Curre	nt (mA)(typ.)	Reflected	Max.	Efficiency
Model Number	Nominal (Range)	Max*	Voltage (VDC)	Max.	Min.	@Max. Load	@No Load	Ripple Current (mA,typ.)	Capacitive Load# (µF)	(%, typ.) <u>@Max. Loa</u>
PWA2405MD-3W			±5	±300	±30	164			680	76
PWA2412(M)D-3W			±12	±125	±12	156	20 20		330	80
PWA2415(M)D-3W			±15	±100	±10	156		220	80	
PWB2403(M)D-3W			3.3	909	91	169		20	2200	74
PWB2405(M)D-3W	24 (9.0-36)	40	5	600	60	164			1000	76
PWB2409(M)D-3W	(6.6 66)		9	333	33	160			680	78
PWB2412(M)D-3W		12 15 24	12	250	25	156			470	80
PWB2415(M)D-3W			15	200	20	156			330	80
PWB2424(M)D-3W			24	125	12	160			220	78
PWA4805D-3W			±5	±300	±30	82			680	76
PWA4812MD-3W			±12	±125	±12	78	10	330	80	
PWA4815MD-3W			±15	±100	±10	78			220	80
PWB4803MD-3W	48	90	3.3	909	91	84	10		2200	74
PWB4805(M)D-3W	(18-72)	(18-72) 80	5	600	60	80	10	15	1000	78
PWB4809(M)D-3W			9	333	33	80			680	78
PWB4812MD-3W	1		12	250	25	78	1		470	80
PWB4815MD-3W			15	200	20	78			330	80

Note: 1.*Input voltage can't exceed this value, or will cause the permanent damage.

2.# For each output.

INPUT SPECIFICATIONS						
Item	Test Conditions	Min.	Тур.	Max.	Unit	
Input Surge Voltage (1sec. max.)	24VDC Input Models	-0.7		50		
	48VDC Input Models	-0.7		100	VDC	
0	24VDC Input Models		8.5	9		
Start-up Voltage	48VDC Input Models		17	18		
Short Circuit Input Power			1.5		W	
Input Filter			πF	ilter		

OUTPUT SPECIFICATION	DNS				
Item	Test Conditions	Min.	Тур.	Max.	Unit
Output Power		0.3		3	W
Positive voltage accuracy			±1	±3	
Negative voltage accuracy	ive voltage accuracy Refer to recommended circuit		±3	±5	
Output Voltage Balance	Dual Output, Balanced Loads		±0.5	±1	%
Line Regulation	Full load, Input voltage from low to high		±0.2	±0.5	
Load Regulation	10% to 100% load		±0.5	±1	
Transient Recovery Time	25%~ 50%~25% load or		15	25	ms
Transient Response Deviation	ransient Response Deviation 50%~75%~50% load step change			±5	%
Temperature Drift	100% load			±0.03	%/°C
Ripple & Noise*	20MHz Bandwidth		75	150	mVp-p
Short Circuit Protection	rcuit Protection Continuous, automatic recovery				
	·	·			

Note: 1.Dual output models unbalanced load: ±5%.

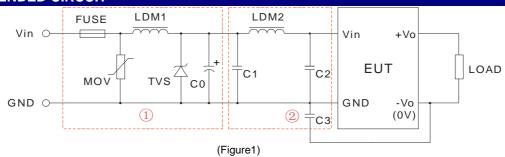
2.*Test ripple and noise by "parallel cable" method. See detailed operation instructions at Testing of Power Converter section, application notes.

COMMON SPECIFICATIONS							
Item	Test Conditions	Min.	Тур.	Max.	Unit		
Isolation Voltage	Tested for 1 minute and leakage current less than 1 mA	1500	-	-	VDC		
Isolation Resistance	Test at 500VDC	1000	Z \	-	ΜΩ		
Isolation Capacitance	Input/Output,100KHz/1V	1	100		pF		
Switching Frequency	Full load, nominal input	7	300		KHz		
MTBF	MIL-HDBK-217F@25°C	1000	1 -	/	K hours		
Case Material		D: Plast	tic (UL94-V0);	MD: Steel, nic	kel plated		
Weight			15		g		

ENVIRONMENTAL SPECIFICATIONS							
Item	Test Conditions	Min.	Тур.	Max.	Unit		
Storage Humidity	Non condensing			95	%		
Operating Temperature	Power derating (above 71°C)	-40		85			
Storage Temperature		-55		125	°C		
Temp. rise at full load	Ta=25°C		15				
Lead Temperature	1.5mm from case for 10 seconds			300			
Cooling			Free	air convection			

EMC SPECIFICATIONS							
EMI	CE	CISPR22/EN55022 CLASS A (External Circuit Refer to Figure1-22)					
EIVII	RE	CISPR22/EN55022 CLASS A (External Circuit Refer to Figure1-22)					
	ESD	IEC/EN61000-4-2 Contact ±4KV perf. Criteria B					
EMS	EFT	IEC/EN61000-4-4 ±2KV perf. Criteria B (External Circuit Refer to Figure 1-①)					
	Surge	IEC/EN61000-4-5 ±2KV perf. Criteria B (External Circuit Refer to Figure 1-①)					

EMC RECOMMENDED CIRCUIT



 $PWA_(M)D\text{-}3W \ recommended \ external \ circuit \ parameters:}$

	Model	PWA24_D-3W	PWA24_MD-3W	PWA48_D-3W	PWA48_MD-3W	
	FUSE		Choose according to	practical input current		
	MOV	10D560K		10D101K		
EMS	LDM1		56	JH		
	TVS	SMCJ48A		SMCJ90A		
	C0	120µl	F/50V	120µF/100V		
	C1	4.7μF/50V	2.2µF/50V	2.2µF/100V	2.2µF/100V	
EMI	LDM2	3.3µH	10μH	10μH	10μH	
⊏IVII	C2	2.2µF/50V	4.7µF/50V	4.7µF/100V	4.7µF/100V	
	C3	100pF/2KV		100pF/2KV	100pF/2KV	

PWB_(M)D-3W recommended external circuit parameters:

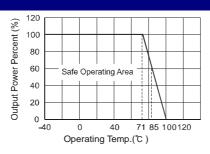
N	/lodel	PWB24_D-3W	PWB24_MD-3W	PWB48_D-3W	PWB48_MD-3W	
	FUSE		Choose according to	ractical input current		
	MOV	10D560K		10D101K		
EMS	LDM1					
	TVS	SMC	J48A	SMCJ90A		
	C0	120µF	-/50V	120µF/100V		
	C1	1μF/50V	4.7µF/50V	2.2μF/100V	4.7μF/100V	
	LDM2	12µH	10μH	10μH	4.7µF/100V	
EMI	C2			4.7µF/100V		
	C3			100pF/2KV	\	

Note: 1. In Figure 1,part①is EMS Recommended external circuit, part②is EMI recommended external circuit. Choose according to requirements.

2. If there is no recommended parameters, the model no require the external component.

EMC RECOMMENDED CIRCUIT PCB LAYOUT PMA_(M) D-3W PWB_(M) D-3W PWB_(M) D-3W PWB_(M) D-3W FUSE GND (Figure 2)

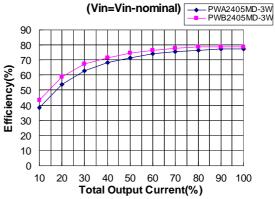
PRODUCT TYPICAL CURVE



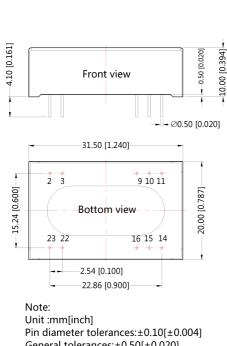
Efficiency VS Input Voltage curve - PWA2405MD-3W - PWB2405MD-3W (Full Load) 85 82 79 76 Efficiency(%) 73 70 67 64 61 58 55 9 12 15 18 21 24 27 30 33

Input Voltage(V)

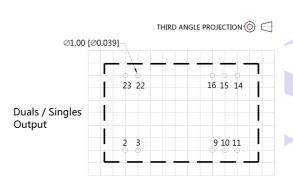
Efficiency VS Output Load curve



OUTLINE DIMENSIONS, RECOMMENDED FOOTPRINT & PACKAGING



General tolerances: $\pm 0.50[\pm 0.020]$



Note:Grid 2.54*2.54mm

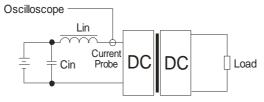
Pin-Out						
Pin	Single	Dual				
2, 3	GND	GND				
9	NC	0V				
10,15	NC	NC				
11	NC	-Vo				
14	+Vo	+Vo				
16	0V	0V				
22,23	V in	V in				

NC: No connection

TEST CONFIGURATIONS

Input Reflected-Ripple Current Test Setup

Input reflected-ripple current is measured with an inductor Lin and Capacitor Cin to simulate source impedance.



Lin(4.7µH) Cin(220 μ F, ESR < 1.0 Ω at 100 KHz)

DESIGN CONSIDERATIONS

1) Requirement on output load

To ensure this module can operate efficiently and reliably, During operation, the minimum output load could not be less than 10% of the full load. If the actual output power is very small, please connect a resistor with proper resistance at the output end in parallel to increase the load, or use our company's products with a lower rated output power.

2) Overload Protection

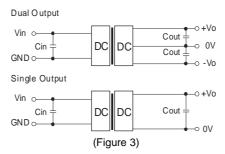
Under normal operating conditions, the output circuit of these products has no protection against overload. The simplest method is add a circuit breaker to the circuit.

3) Recommended circuit

All the PWA_(M)D-3W & PWB_(M)D-3W Series have been tested according to the following recommended testing circuit before leaving factory. This series should be tested under load (see Figure 3).

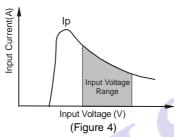
If you want to further decrease the input/output ripple, you can increase a capacitance properly or choose capacitors with low ESR (see Figure 4). If the capacitance is too big, a startup problem might arise. For every channel of output, provided the safe and reliable operation is ensured, the greatest capacitance of its filter capacitor must less than the Max. Capacitive Load. If you want to use the products in high EMI, please choose our metal packaged products (PWA_MD-3W&PWB_MD-3W).

General: Cin: 24V&48V 10μF~47μF Cout: 10μF/100mA



4) Input current

Nominal input voltage range. The input current of the power supply must be sufficient to the startup current (Ip) of the DC/DC module (Figure 4). General: Ip ≤1.4*lin-max



5) Cannot use in parallel and hot swap

Note:

- 1. The load shouldn't be less than 10%, otherwise ripple will increase dramatically. Operation under minimum load will not damage the converter; However, they may not meet all specification listed.
- 2. Max. Capacitive Load tested at input voltage range and full load.
- 3. All specifications measured at Ta=25°C, humidity<75%, nominal input voltage and rated output load unless otherwise specified.
- 4. In this datasheet, all the test methods of indications are based on our corporate standards.
- 5. All characteristics are for listed model only, non-standard models may perform differently, please contact our technical person for more detail.
- 6. Contact us for your specific requirement.
- 7. Specifications subject to change without prior notice.

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