

FXXXXS-1W:

3KV ISOLATION, 1W OUTPUT, SIP 7 PACKAGE, UNREGULATED, MTBF>1M HOURS

Available Inputs:

5, 12, 15 and 24 VDC

Available Outputs:

3.3, 5, 9, 12, 15, 18 and 24 VDC

Other specifications please enquire Sunyuan Technology.



Electrical Specifications

(Typical at + 25° C, nominal input voltage, rated output current unless otherwise specified)

Input Specifications

Voltage range	+/- 10 %
Filter	Capacitors
Isolation Specifications	
Rated voltage	3000 VDC
Leakage current	1 mA
Resistance	10 ⁹ Ohm
Capacitance	60 pF type.

Output Specifications

Voltage accuracy	+/- 5 %, max.
Ripple and noise (at 20 MHz BW)	75 mV p-p, max.
Short circuit protection	Momentary
Line voltage regulation	+/- 1,2 % / 1,0 % of Vin
Load voltage regulation	+/- 8 %, load = 20 ~ 100 %
Temperature coefficient	+/- 0,02 % / °C

General Specifications

Efficiency	70 % to 85 %
Switching frequency	125 KHz, type.

Environmental Specifications

Operating temperature (ambient)	- 40° C to + 85° C
Storage temperature	- 55 °C to + 125 °C
Derating	See graph
Humidity	Up to 90 %, non condensing
Cooling	Free air convection

Physical Characteristics

Dimensions SIP	19,50 x 6,00 x 9,50 mm 0,76 x 0,24 x 0,37 inches
Weight	2 g, 3 g for the 48 VDC variants
Case material	Non conductive black plastic

Examples of Partnumbers

SUNYUAN PART NO.	INPUT VOLTAGE (VDC)	INPUT CURRENT NO LOAD	INPUT CURRENT FULL LOAD	OUTPUT VOLTAGE (VDC)	OUTPUT CURRENT (max. mA)	EFFICIENCY FULL LOAD (% TYPE.)
F0503S-1W	5	14	363	3.3	303	72
F0505S-1W	5	26	266	5	200	75
F0512S-1W	5	18	254	12	84	70
F1205S-1W	12	11	119	5	200	70
F1212S-1W	12	10	104	12	84	80
F1215S-1W	12	15	107	15	68	78
F2405S-1W	24	5	55	5	200	75
F2412S-1W	24	7	54	12	84	77
F2415S-1W	24	8	55	15	76	76

DC-DC CONVERTER

**FXXXS-1W:
3KV ISOLATION,**

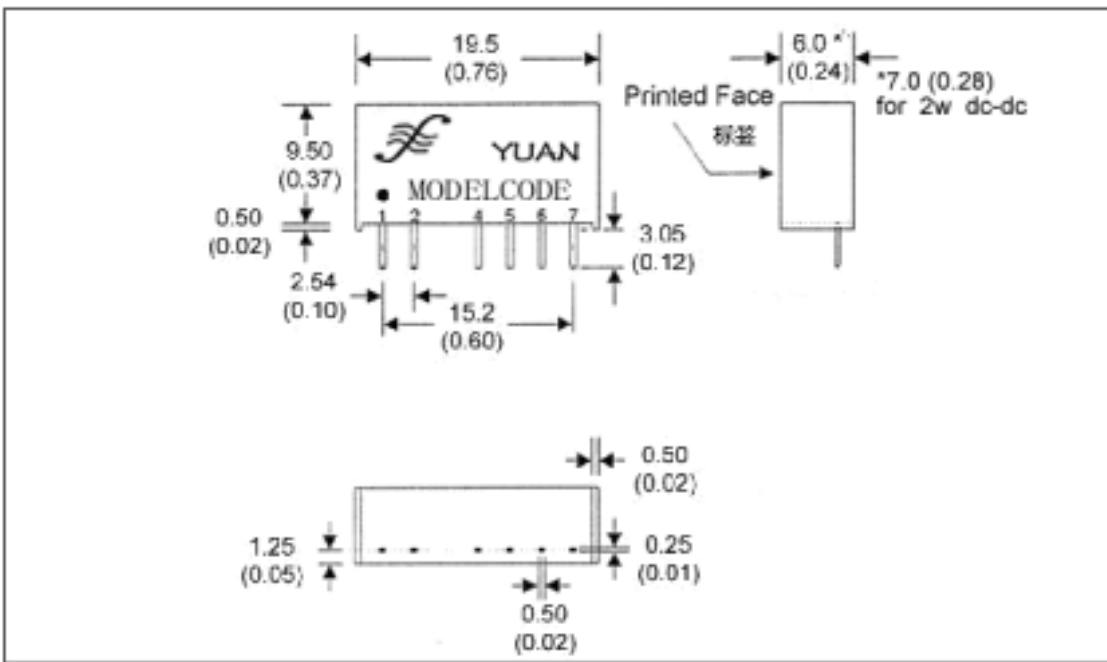
1W OUTPUT,

SIP 7 PACKAGE,

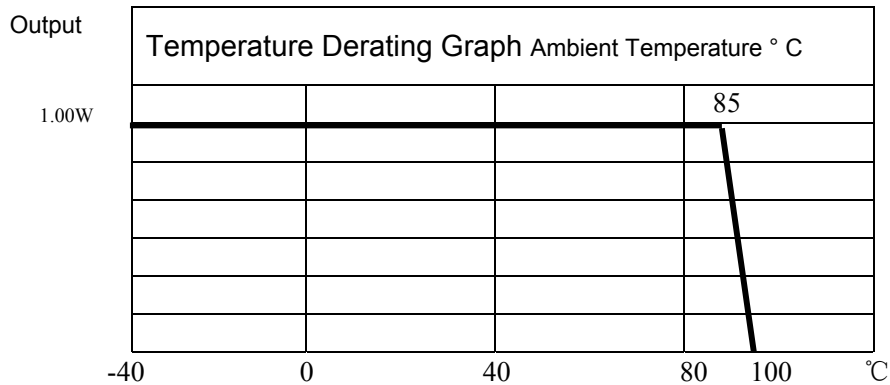
UNREGULATED,

MTBF>1M HOURS

Dimensions



Derating Graph and Pinning



Pin	Connection		
1	+	V _{in}	Input
2	-	V _{in}	Input
3	No	Pin	Omitted
4	No	Pin	Omitted
5	-	V _{out}	Output
6	No	Pin	Omitted
7	+	V _{out}	Output

